

Network

```
graph TD; Network[Network] --- AM1[Attraction memory]; Network --- AM2[Attraction memory]; AM1 --- C1[Cache]; AM2 --- C2[Cache]; C1 --- P1[Processor]; C2 --- P2[Processor]; P1 --- Ellipsis[...];
```

Attraction
memory

Attraction
memory

Cache

Cache

Processor

Processor